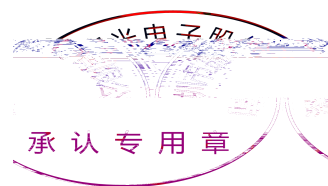


SPECIFICATION



REFOND P/N
RF-A2E3C-2W2H-B1

比; ;
Mass Production



1. Description

1.1

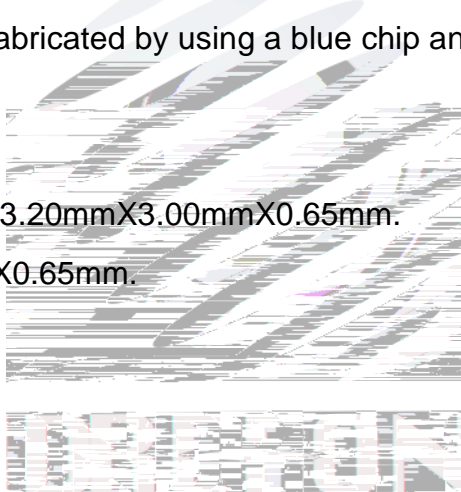


The White LED, which was fabricated by using a blue chip and the phosphor.

The Yellow LED, which was fabricated by using a blue chip and the phosphor.

The LED package dimension: 3.20mmX3.00mmX0.65mm.

3.20mmX3.00mmX0.65mm.



1.2 Features

EMC Package.

Extremely wide viewing angle.

Suitable for all SMT assembly and solder process.

Available on tape and reel.

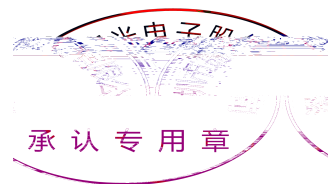
Moisture sensitivity level: Level 2.

Compliance with RoHS and REACH.

Qualifications: The product qualification test plan is based on the guidelines of AEC-Q101 Stress Test Qualification for Automotive Grade Discrete Semiconductors
AEC-Q101

1.3 Application

Automotive Lighting Interior and Exterior.

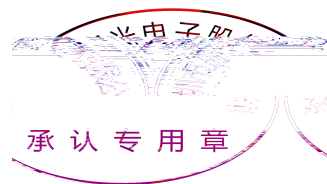


Electrical / Optical Characteristics at Ts=25°C

Symbol	Test Condition	Value			Unit
		Min.	Typ.	Max.	
V _F	I _F =350mA	2.8	3.1	3.4	V
I _R	V _R =5V	---	---	10	uA
	I _F =350mA	117	137	160	lm
	I _F =350mA	83.7	102	117	lm
	I _F =350mA	---	120	---	deg
THJ-S	I _F =350mA	---	---	20	W

2 Absolute Maximum Ratings at Ts=25°C

	Symbol	Rating	Units
	P _D	2720	mW
	I _F	400	mA
Peak Forward Current	I _{FP}	700	mA
Reverse Voltage	V _R	5	V
Electrostatic Discharge (HBM)	E _{SD}	2000	V
Operating Temperature	T _{OPR}	-40	



Notes

1. 1/10 Duty cycle, 10ms pulse width.
2. The above forward voltage measurement allowance tolerance is $\pm 0.1V$! %M%
3. The above color coordinates measurement allowance tolerance is ± 0.005 . ±
4. The above luminous intensity measurement allowance tolerance $\pm 10\%$(' fl %
5. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
6. All measurements were made under the standardized environment of Refond.
7. When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate
8. ESD yield is over 90% at 2000V ESD (HBM). ESD protection during products handling is needed.

1.6 Bin Range Of Forward Voltage and Luminous Flux (IF=350mA)
BIN (IF=350mA)

Table 1-3

VF V	G1	G2	H1	H2	I1	I2
White	2.8-2.9	2.9-3.0	3.0-3.1	3.1-3.2	3.2-3.3	3.3-3.4
Im	SB	TA	TB			
White	117-130	130-144	144-160			



1.7 Typical Optical Characteristics Curves

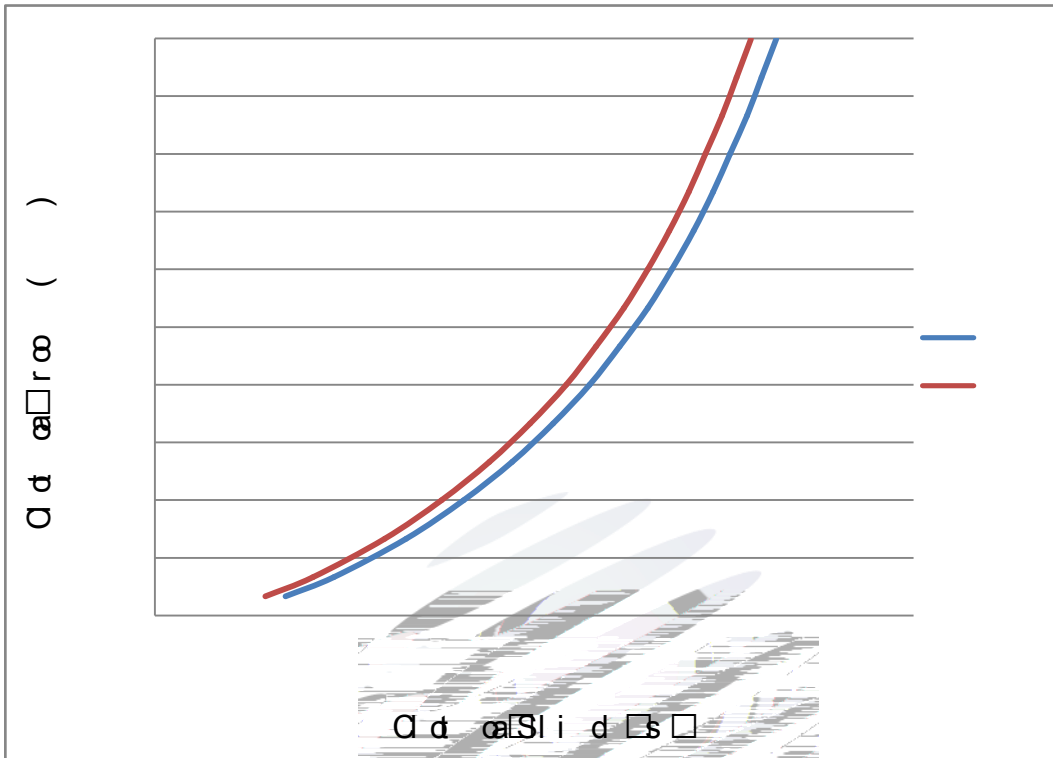


Fig. 1-7 Forward Voltage Vs Forward Current

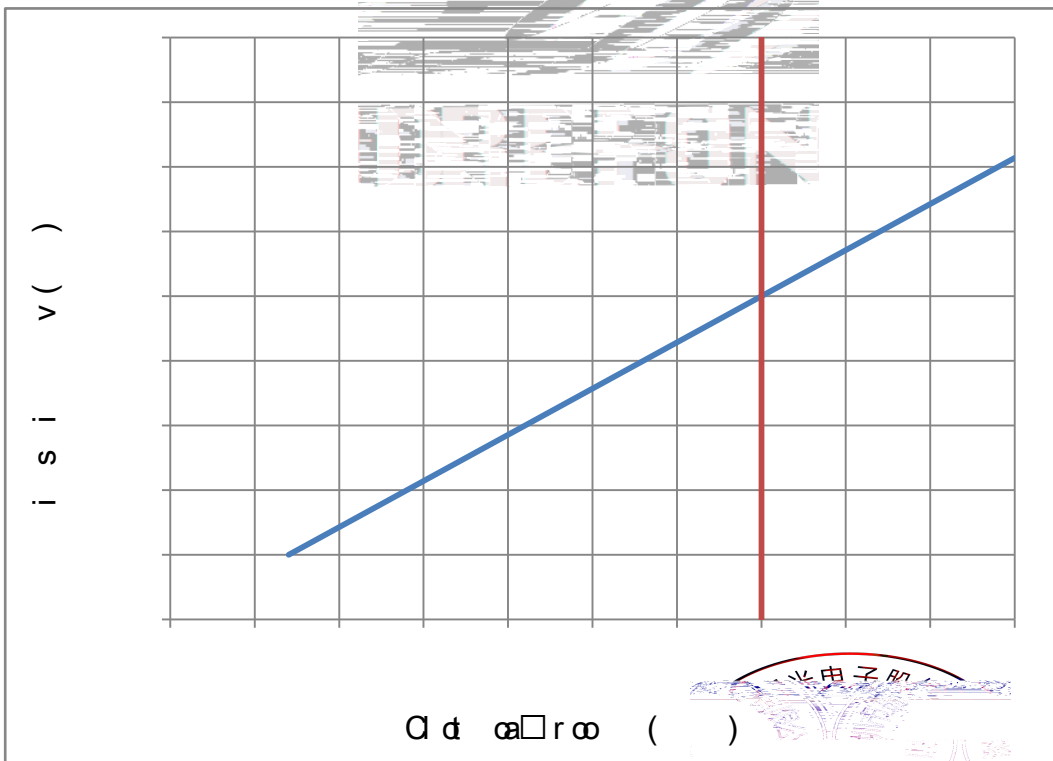


Fig. 1-8 Forward Current Vs Relative Intensity

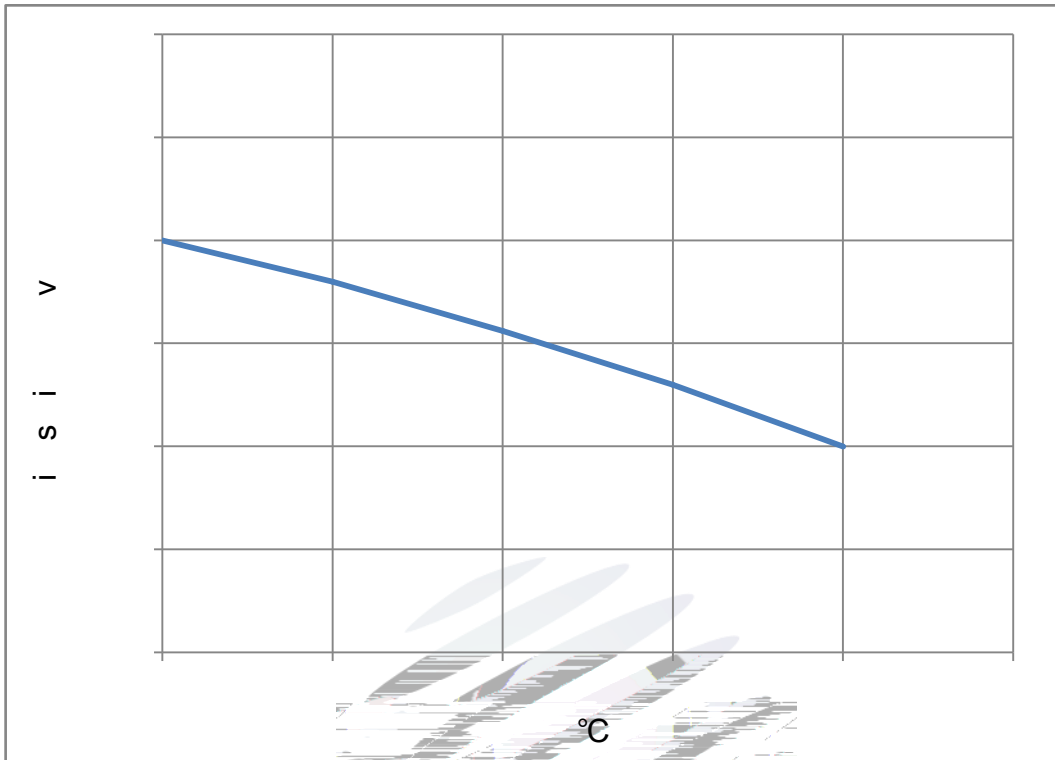


Fig. 1-9 Solder Temperature Vs Relative Intensity

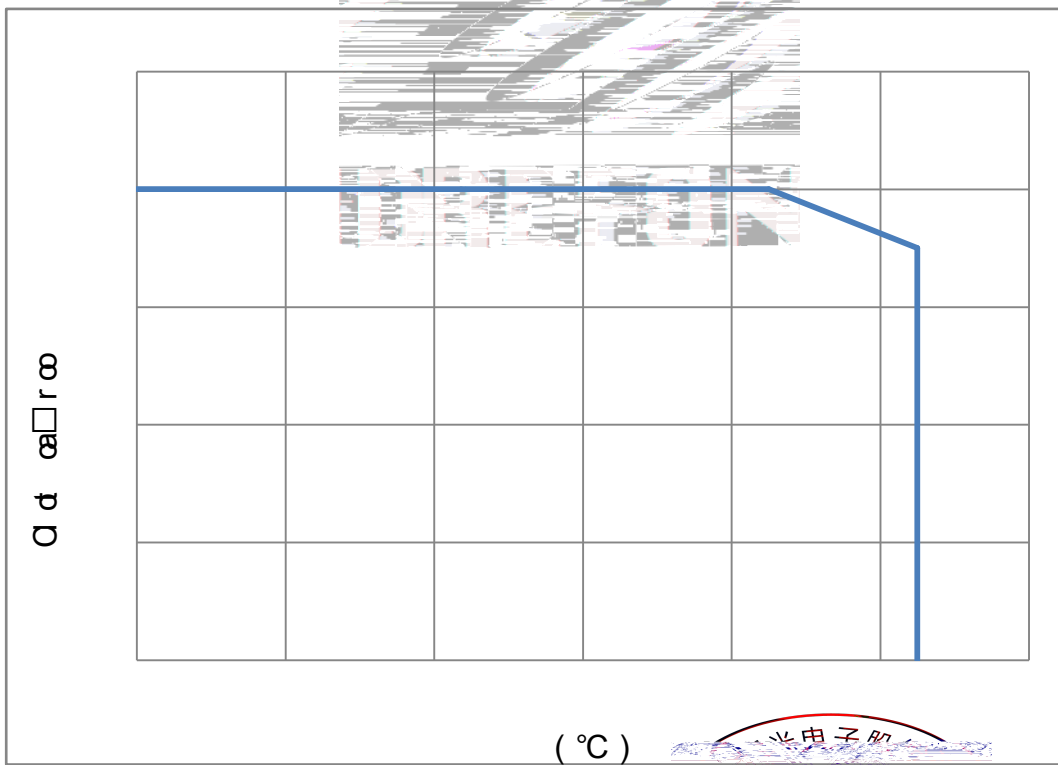
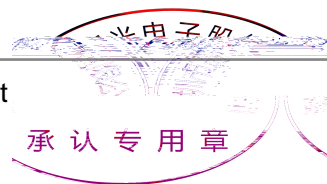


Fig. 1-10 Solder Temperature Vs Forward Current



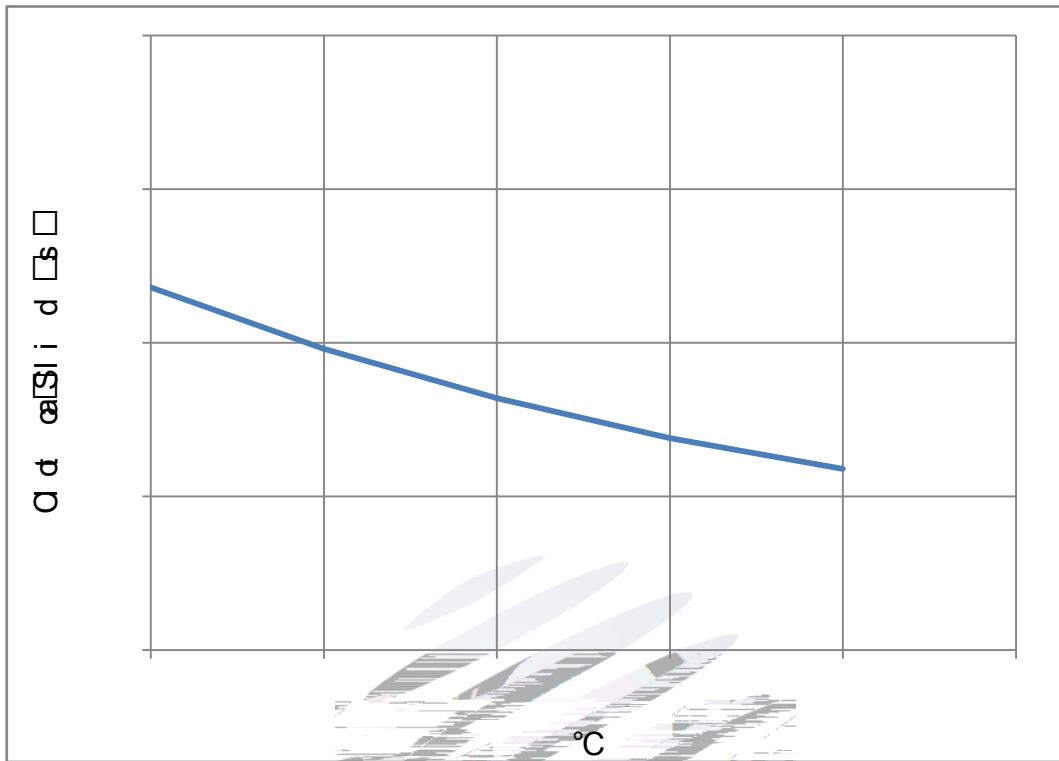


Fig. 1-11 Forward Voltage Vs Solder Temperature

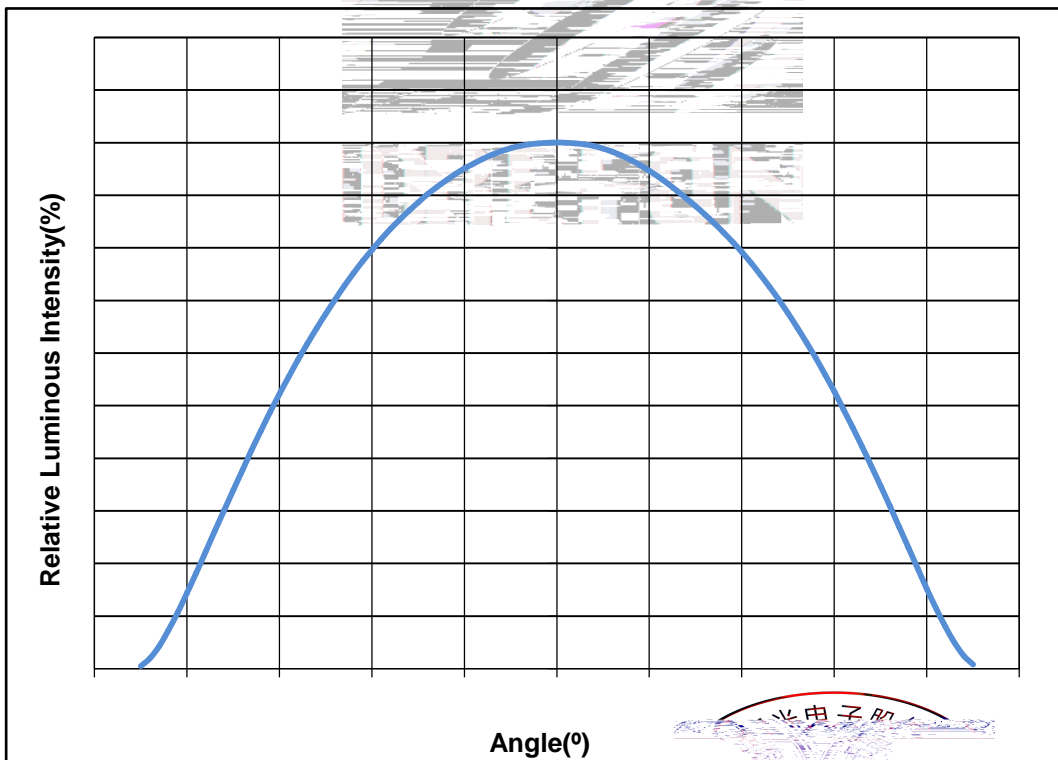
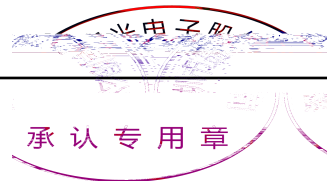


Fig. 1-12 Radiation diagram



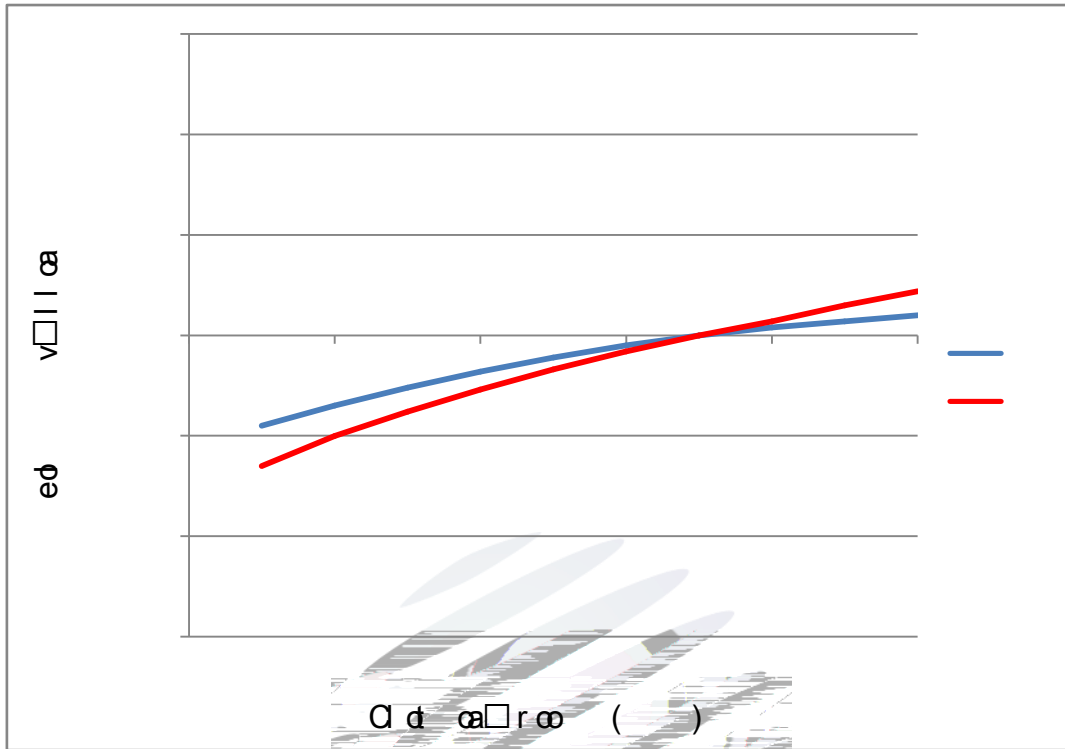


Fig. 1-13 Chromaticity Coordinate Shift Vs Forward Current (White) ()

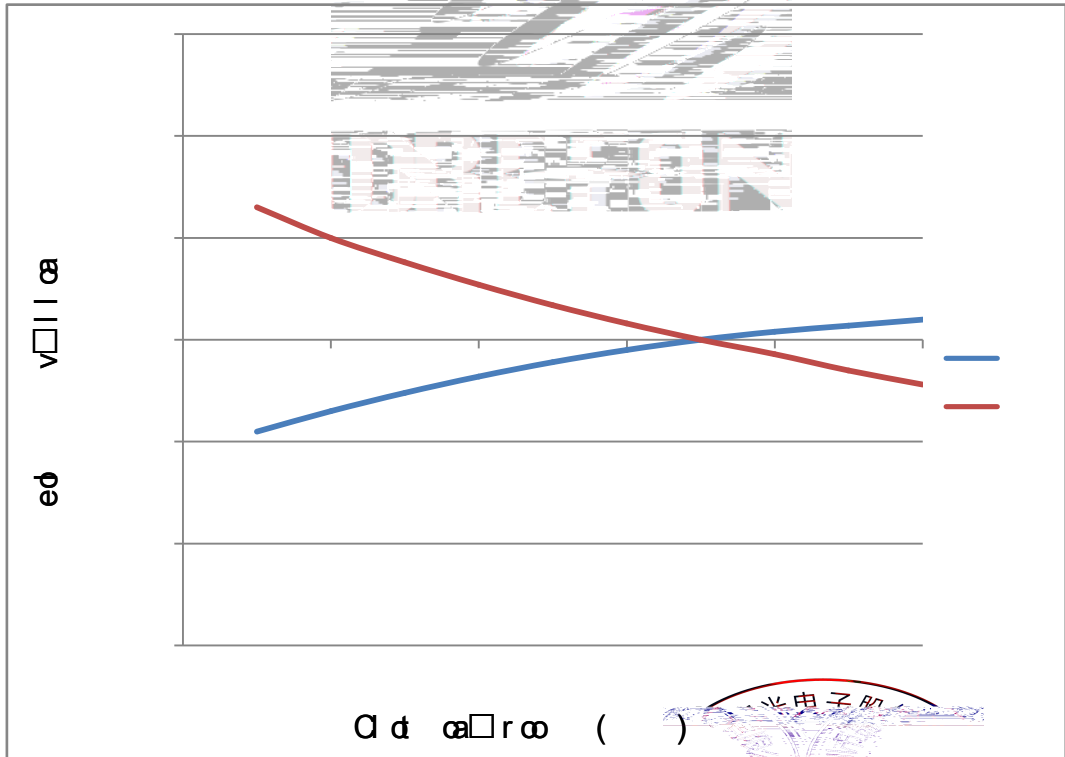
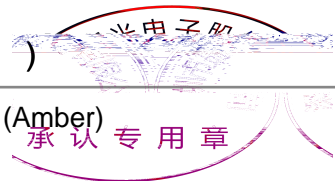


Fig. 1-13 Chromaticity Coordinate Shift Vs Forward Current (Amber) ()



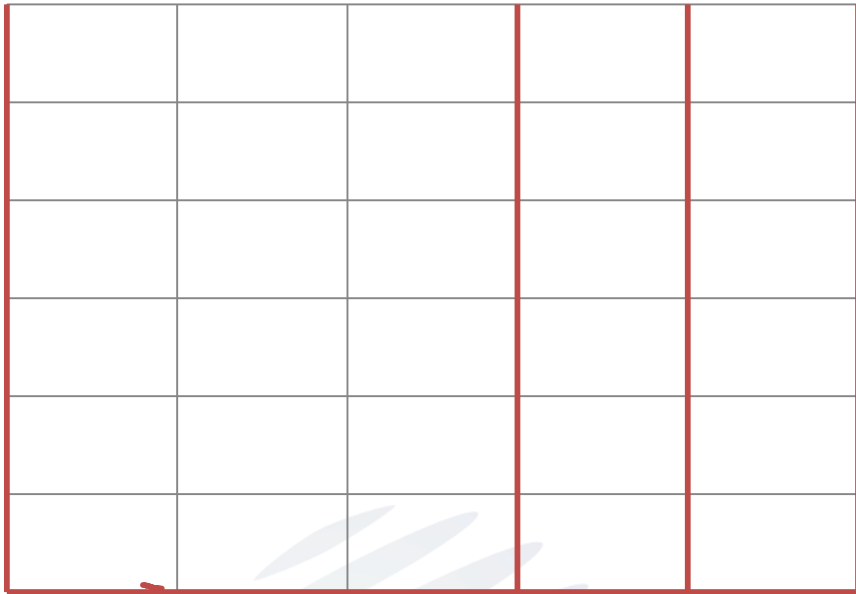


Fig. 1-14 Spectrum Distribution(White)

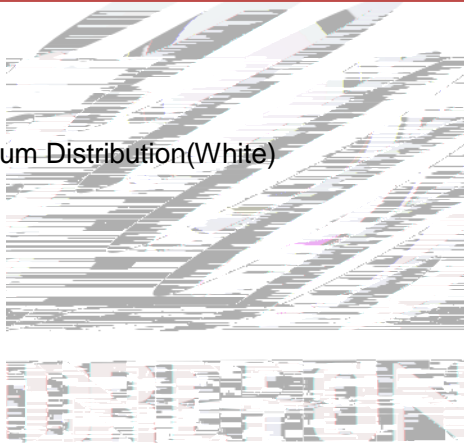
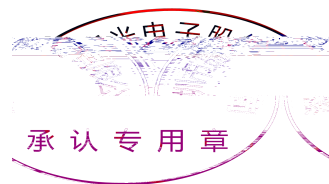


Fig. 1-14 Spectrum Distribution(Amber)



2. Packaging

2.1 Packaging Specification

Package:4000pcs/reel. 4000pcs

2.1.1 Carrier Tape Dimension

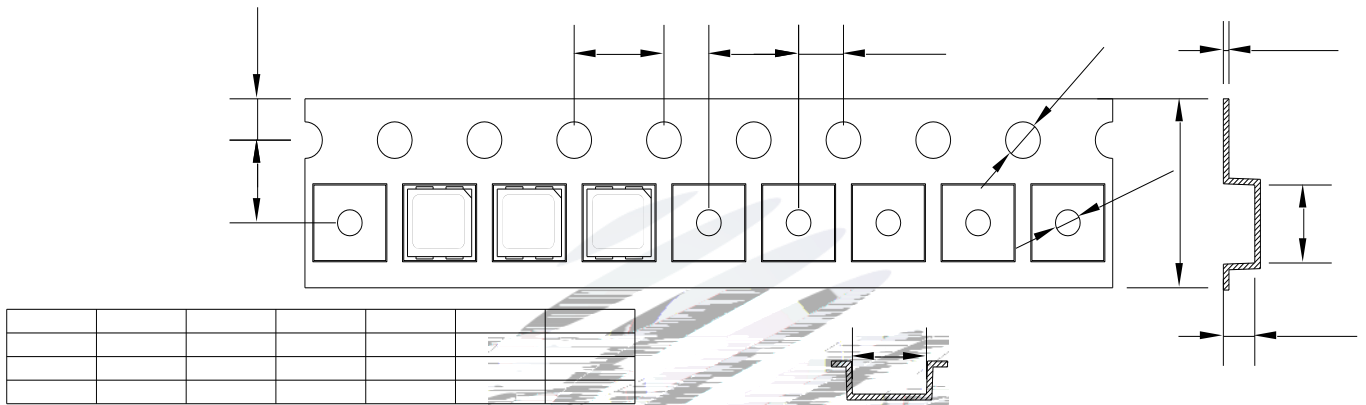


Fig.2-1 Carrier Tape Dimension

2.1.2 Reel Dimension

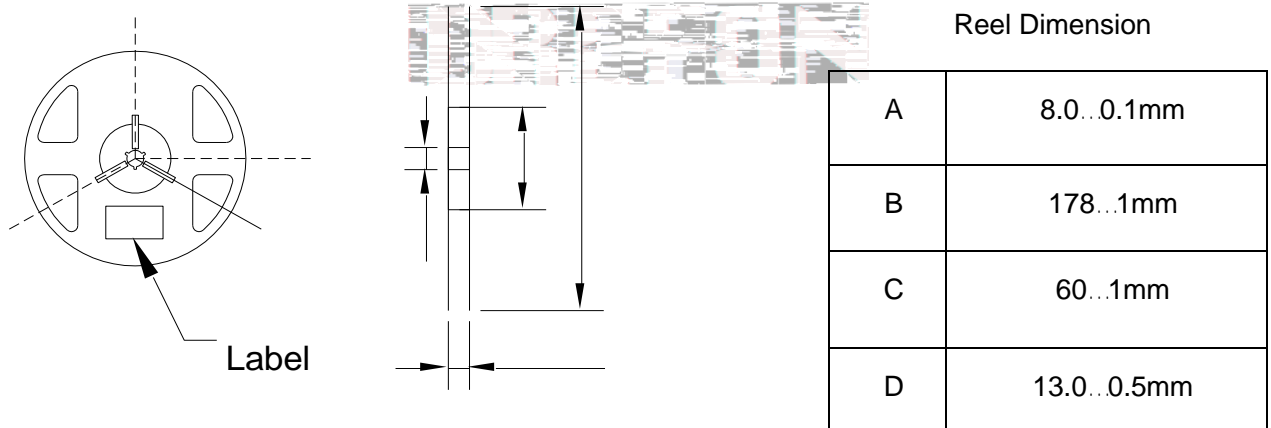
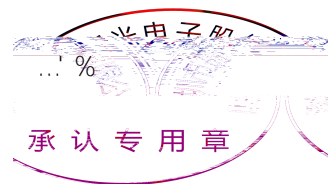


Fig.2-2 Reel Dimension

Notes

The tolerances unless mentioned $\pm 0.1\text{mm}$. Unit : mm



2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref.Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2times	20pcs.	0/1
Thermal Shock	JEITAED-4701 300307	-40 15min 10s 125 15min	1000 cycle	20pcs.	0/1
High Temperature Storage	JEITAED-4701 200 201	Temp:125	1000hrs.	20pcs.	0/1
Low Temperature Storage	JEITA ED-4701 200 202	Temp:-40	1000hrs.	20pcs.	0/1
Life Test	JESD22-A108	Ta=25 If=350mA	1000hrs.	20pcs.	0/1
High Temperature High Humidity Life Test	JESD22-A101	85 / 85%RH If=350mA	1000hrs.	20pcs.	0/1
Temperature Humidity Storage	JEITA ED-4701 100 103	TA=85 RH=85%	1000hrs.	20pcs.	0/1

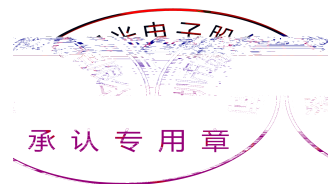
2.5 Criteria For Judging Damage

Table 2-4 Criteria For Judging Damage

Test Items	Symbol	Test Condition	Criteria For Judgement	
			Min.	Max.
Forward Voltage	V_F	$I_F=350mA$	-	U.S.L*)x1.1

Reverse Current

I_R V_R

3. SMT Reflow Soldering Instructions SMT

3.1 SMT Reflow Soldering Instructions SMT

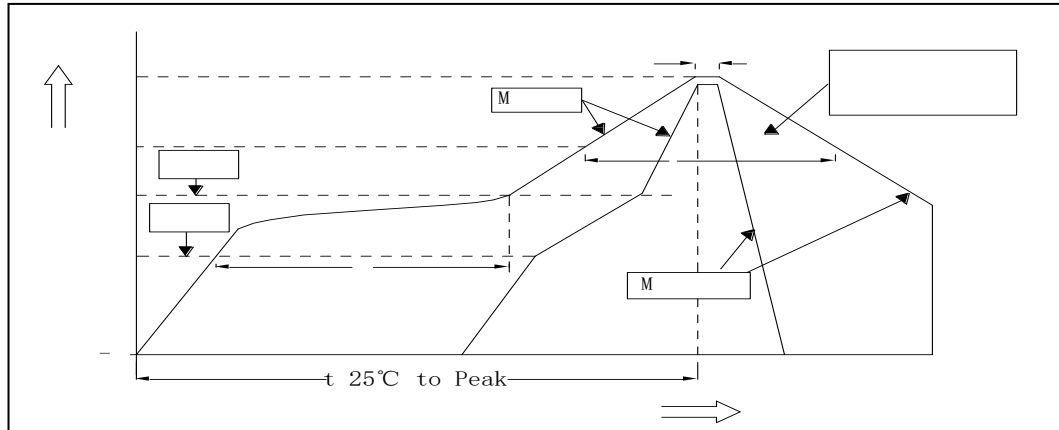


Fig.3-1 SMT Reflow Soldering Instructions SMT

Table 3-1 Reflow parameters

Average temperature rise speed	T_{smax}	T_P	3 °C/	Max 3 °C/ s
Preheating: minimum temperature	(T_{smin})		150 °C	
Preheating: Max temperature	(T_{smax})		200 °C	
Preheating: Time	T_{smin}	T_{smax}	60 - 120	60s-120s
Time limited to maintain high temperature: the temperature (T_L)	217 °C			
Time limited to maintain high temperature: The Time (t_L)			60	Max 60s
Peak /Classification of temperature:	/	(T_P)	260 °C	
Time limit classification of peak temperature time t_p			10	Max 10s
(T_P) 5 °C	Hold time within		30	Max 30s
5 °C with the actual peak temperature (T_P)				
Cooling speed			6 °C/	Max 6 °C/ s
25 °C	Needed time from 25 °C to T_p		8	Max 8 minutes

承认专用章

Notes

(1)Reflow soldering should not be done more than twice. If more than 24 hours between the two solderings , LED will be damaged.

(2)When soldering , do not put stress on the LEDs during heating.

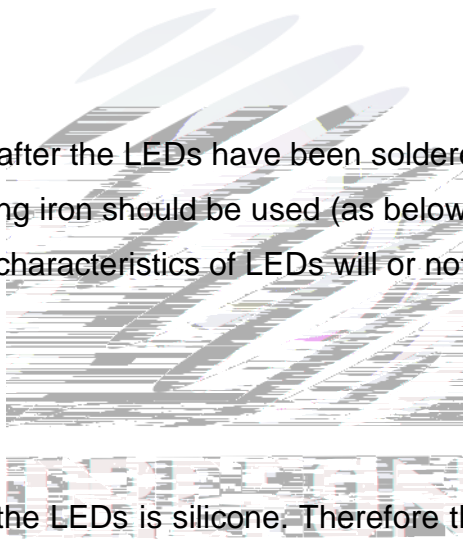
3.1.1 Soldering Iron

(1) When do soldering by hand, keep the temperature of iron below less 300°C less than 3 seconds

(2) Soldering by hand should be done only one time.

3.1.2 Repairing

Repairing should not be done after the LEDs have been soldered. When repairing is unavoidable,a double-head soldering iron should be used (as below figure). It should be confirmed in advance whether the characteristics of LEDs will or not be damaged by repairing.



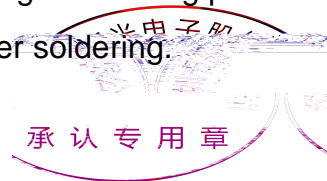
LED

3.1.3 Cautions

The encapsulated material of the LEDs is silicone. Therefore the LEDs have a soft surface on the top of package. The pressure to the top surface will be impacted on the reliability of the LEDs. Precautions should be taken to avoid the strong pressure on the encapsulated part. So when use the picking up nozzle, the pressure on the silicone resin should be proper. LED

(2) Components should not be mounted on warped (non coplanar) portion of PCB. After soldering, do not warp the circuit board.LED

(3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering. Do not rapidly cool device after soldering.



(4) Handle the component along the side surface by using forceps or appropriate tools; Do not directly touch or Handle the silicone lens surface, it may damage the internal circuitry.

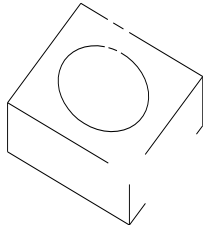
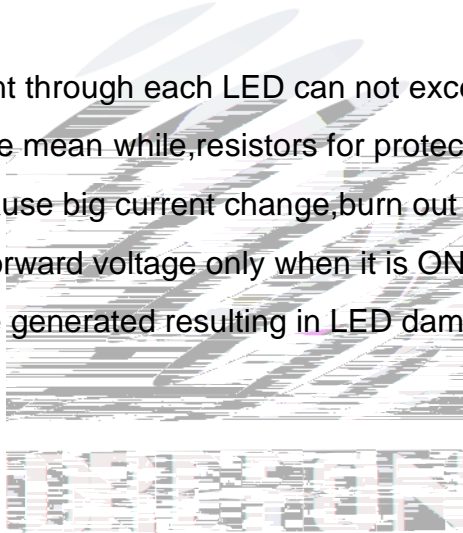


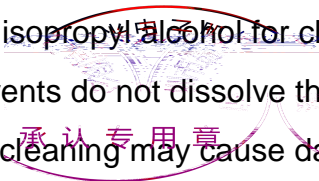
Fig 4-1 Cautions

(5) In designing a circuit, the current through each LED can not exceed the absolute maximum rating specified for each LED. In the mean while, resistors for protection should be applied, otherwise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.



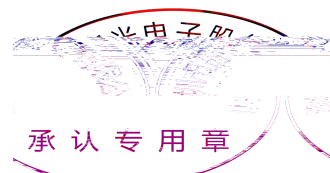
(6) Thermal Design is paramount importance because heat generation may result in the Characteristics decline, such as brightness decreased, Color change and so on. Please consider the heat generation of the LEDs when making the system design. LED

(7) Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust, requiring special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components. Refond suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the





www.refond.com



Declare

This specification is written both in English and in Chinese and the latter is formal.